

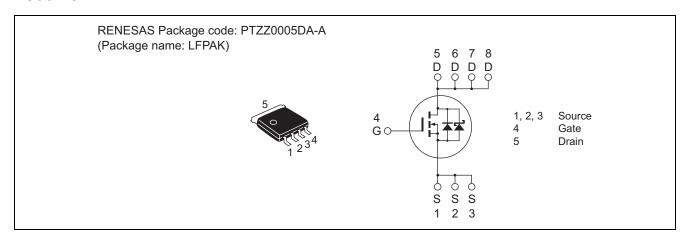
# RJK03C1DPB

# Silicon N Channel Power MOS FET with Schottky Barrier Diode Power Switching REJ03G1830-0310 Rev.3.10 Sep 29, 2009

#### **Features**

- High speed switching
- Capable of 4.5 V gate drive
- Low drive current
- High density mounting
- Low on-resistance  $R_{DS(on)} = 1.7 \text{ m}\Omega \text{ typ. (at } V_{GS} = 10 \text{ V)}$
- Pb-free
- Halogen-free

### **Outline**



# **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	30	V
Gate to source voltage	$V_{GSS}$	±20	V
Drain current	I <sub>D</sub>	60	Α
Drain peak current	I <sub>D(pulse)</sub> Note1	240	Α
Body-drain diode reverse drain current	I <sub>DR</sub>	60	Α
Avalanche current	I <sub>AP</sub> Note 2	28	Α
Avalanche energy	E <sub>AR</sub> Note 2	78.4	mJ
Channel dissipation	Pch Note3	65	W
Channel to Case Thermal Resistance	θch-C	1.92	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1%

- 2. Value at Tch = 25°C, Rg  $\geq$  50  $\Omega$
- 3. Tc = 25°C

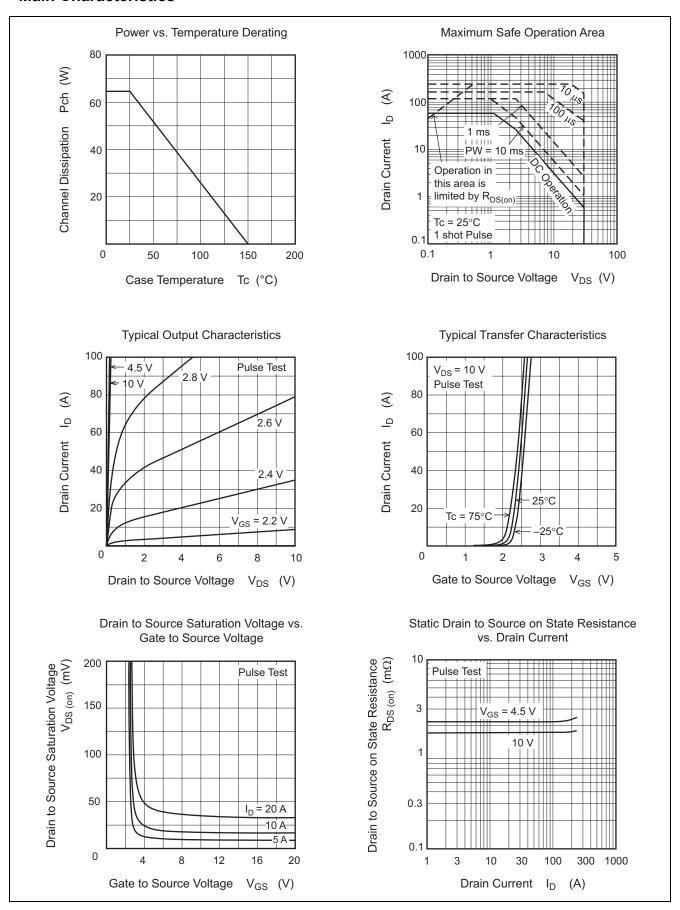
# **Electrical Characteristics**

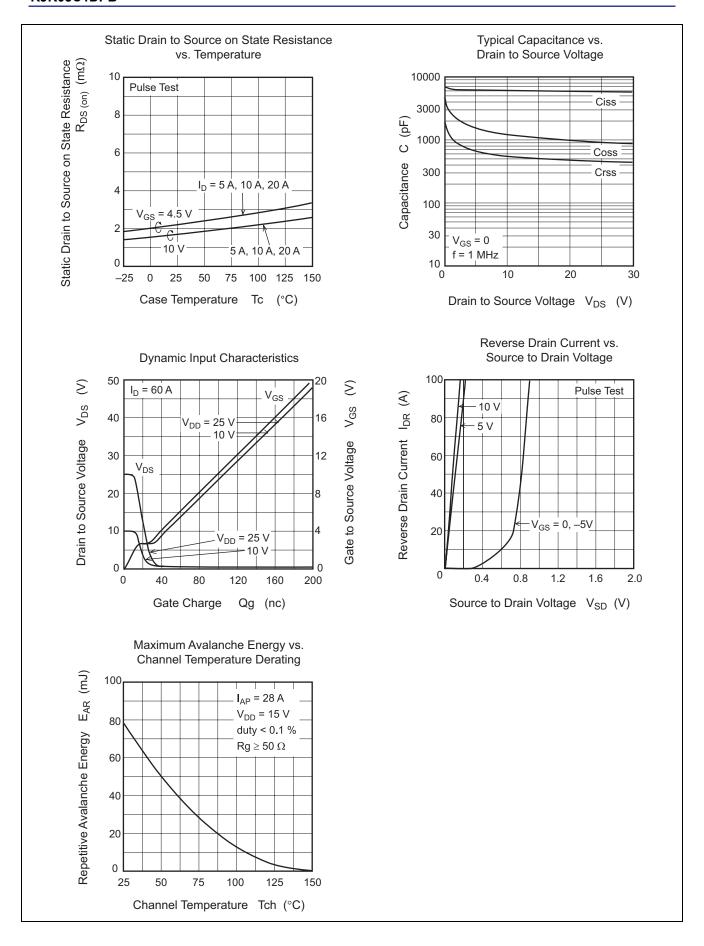
 $(Ta = 25^{\circ}C)$ 

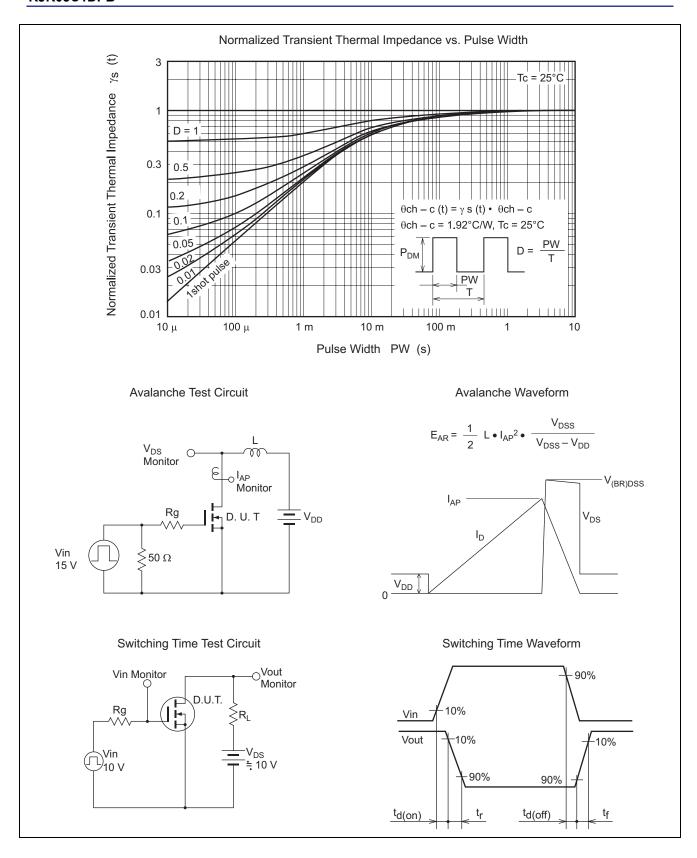
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V <sub>(BR)DSS</sub>	30	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_	_	±0.5	μΑ	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>		_	1	m A	$V_{DS} = 30 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.2	_	2.5	>	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Static drain to source on state	R <sub>DS(on)</sub>	l	1.7	2.2	mΩ	$I_D = 30 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note4}}$
resistance	R <sub>DS(on)</sub>	l	2.2	3.1	mΩ	$I_D = 30 \text{ A}, V_{GS} = 4.5 \text{ V}^{\text{Note4}}$
Forward transfer admittance	y <sub>fs</sub>	l	120	_	S	$I_D = 30 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note4}}$
Input capacitance	Ciss	l	6000	_	pF	$V_{DS} = 10 \text{ V}, V_{GS} = 0,$
Output capacitance	Coss	_	1230		pF	f = 1 MHz
Reverse transfer capacitance	Crss	_	550	_	pF	
Gate Resistance	Rg	_	0.5	_	Ω	
Total gate charge	Qg	_	42	_	nC	$V_{DD} = 10 \text{ V}, V_{GS} = 4.5 \text{ V},$
Gate to source charge	Qgs	_	18	_	nC	I <sub>D</sub> = 60 A
Gate to drain charge	Qgd	_	16	_	nC	
Turn-on delay time	t <sub>d(on)</sub>	_	18	_	ns	$V_{GS} = 10 \text{ V}, I_D = 30 \text{ A},$
Rise time	t <sub>r</sub>	_	19	_	ns	$V_{DD} \cong 10 \text{ V}, R_L = 0.33 \Omega,$
Turn-off delay time	$t_{d(off)}$		75	_	ns	$Rg = 4.7 \Omega$
Fall time	t <sub>f</sub>		15	_	ns	
Body-drain diode forward voltage	$V_{DF}$	_	0.39	_	V	$I_F = 2 A, V_{GS} = 0^{Note4}$
Body-drain diode reverse	t <sub>rr</sub>	_	40	_	ns	I <sub>F</sub> = 60 A, V <sub>GS</sub> = 0
recovery time						$di_F/dt = 100 A/ \mu s$

Notes: 4. Pulse test

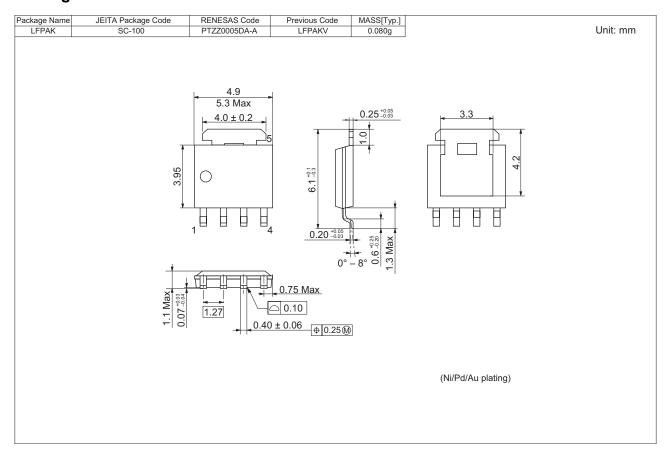
## **Main Characteristics**







# **Package Dimensions**



# **Ordering Information**

Part No.	Quantity	Shipping Container
RJK03C1DPB-00-J5	2500 pcs	Taping

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